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Semiconductor devices - Mechanical and climatic test methods - Part 24: Accelerated moisture resistance - Unbiased HAST

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This edition includes the following significant technical changes with respect to the previous edition:

a) rearrangement of clauses to reposition requirements;
b) addition of two notes to the post-test electrical procedures.

The text of this International Standard is based on the following documents:

Draft	Report on voting
47/2957/FDIS	47/2974/RVD

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this International Standard is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/publications.

A list of all parts of the IEC 60749 series, under the general title *Semiconductor devices – Mechanical and climatic test methods*, can be found in the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under webstore.iec.ch in the data related to the specific document. At this date, the document will be

– reconfirmed,
– withdrawn, or
– revised.

1 Scope

This part of IEC 60749 specifies unbiased highly accelerated stress testing (HAST). HAST is performed for the purpose of evaluating the reliability of non-hermetically packaged solid-state devices in humid environments.

It is a highly accelerated test which employs temperature and humidity under non-condensing conditions to accelerate the penetration of moisture through the external protective material (encapsulant or seal) or along the interface between the external protective material and the metallic conductors which pass through it. Bias is not applied in this test to ensure that the failure mechanisms potentially overshadowed by bias can be uncovered (e.g. galvanic corrosion).

This test is used to identify failure mechanisms internal to the package and is destructive.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60749-5, *Semiconductor devices - Mechanical and climatic test methods - Part 5: Steady-state temperature humidity bias life test*

IEC 60749-33, *Semiconductor devices - Mechanical and climatic test methods - Part 33: Accelerated moisture resistance - Unbiased autoclave*